OIPE JC177
MAY 0 6 2004
PATENT & TRADEMARK OFFICE

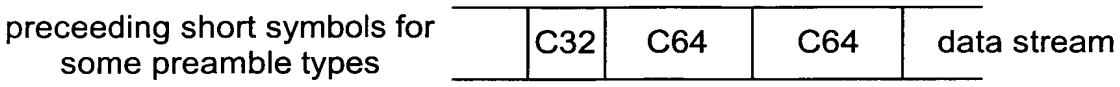

Fig. 1

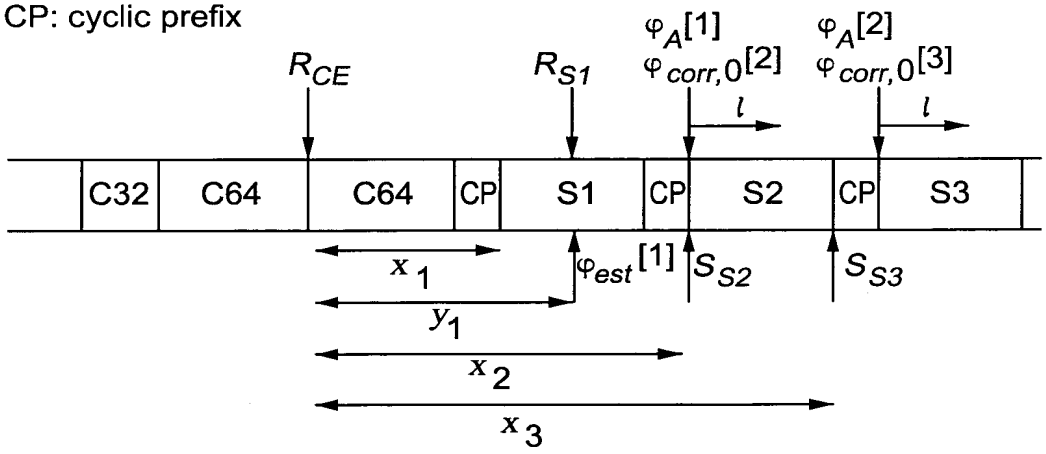

Fig. 2

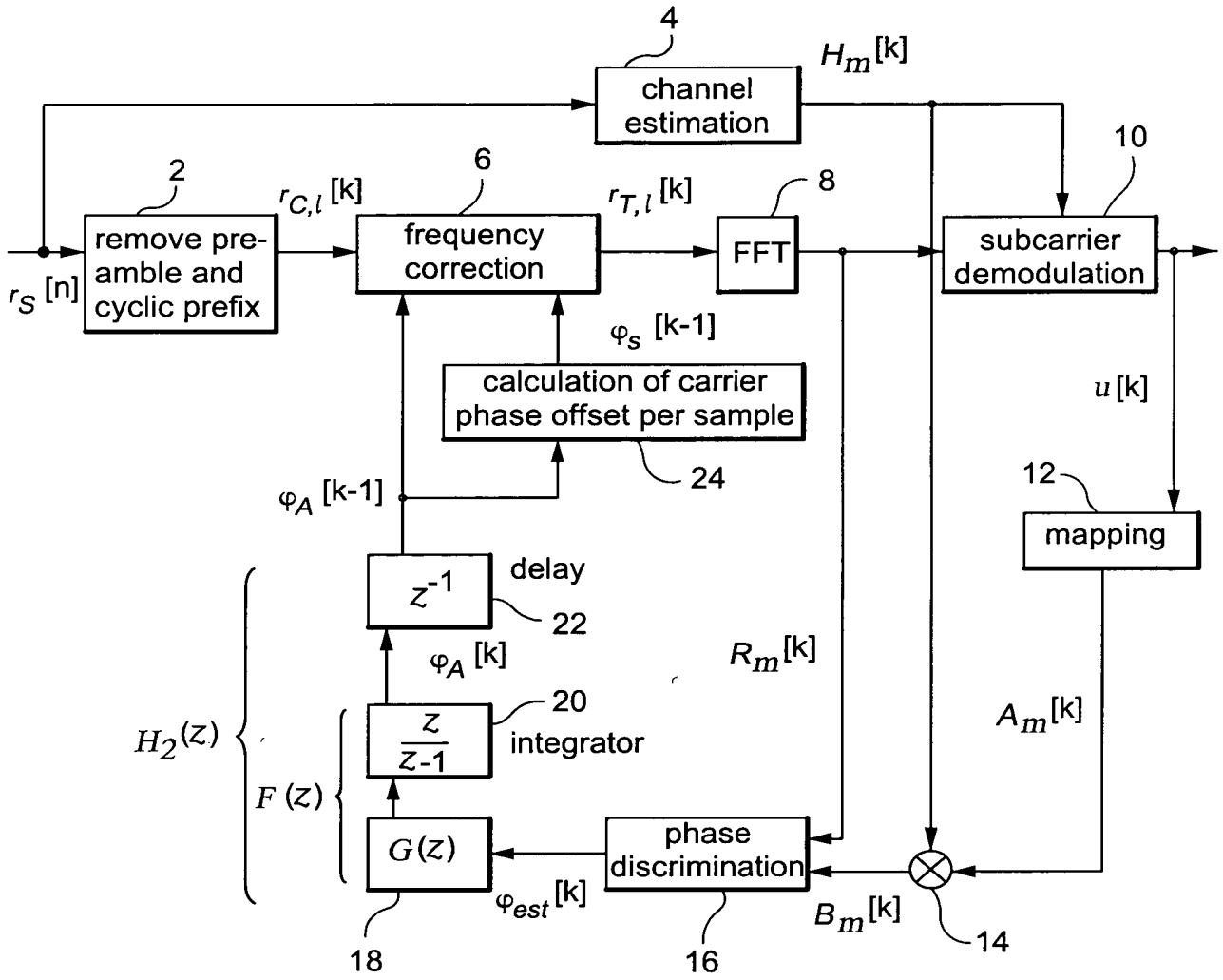
4
channel estimation
H_m[k]
10
2
6
8
$r_{C,l}$ [k]
$r_{T,l}$ [k]
remove pre-amble and cyclic prefix
frequency correction
FFT
subcarrier demodulation
r_S [n]
φ_s [k-1]
calculation of carrier phase offset per sample
24
u[k]
12
φ_A [k-1]
mapping
z^{-1}
delay
22
φ_A [k]
R_m[k]
A_m[k]
20
integrator
$H_2(z)$
$F(z)$
$G(z)$
phase discrimination
φ_{est} [k]
B_m[k]
18
16
14

Fig. 3

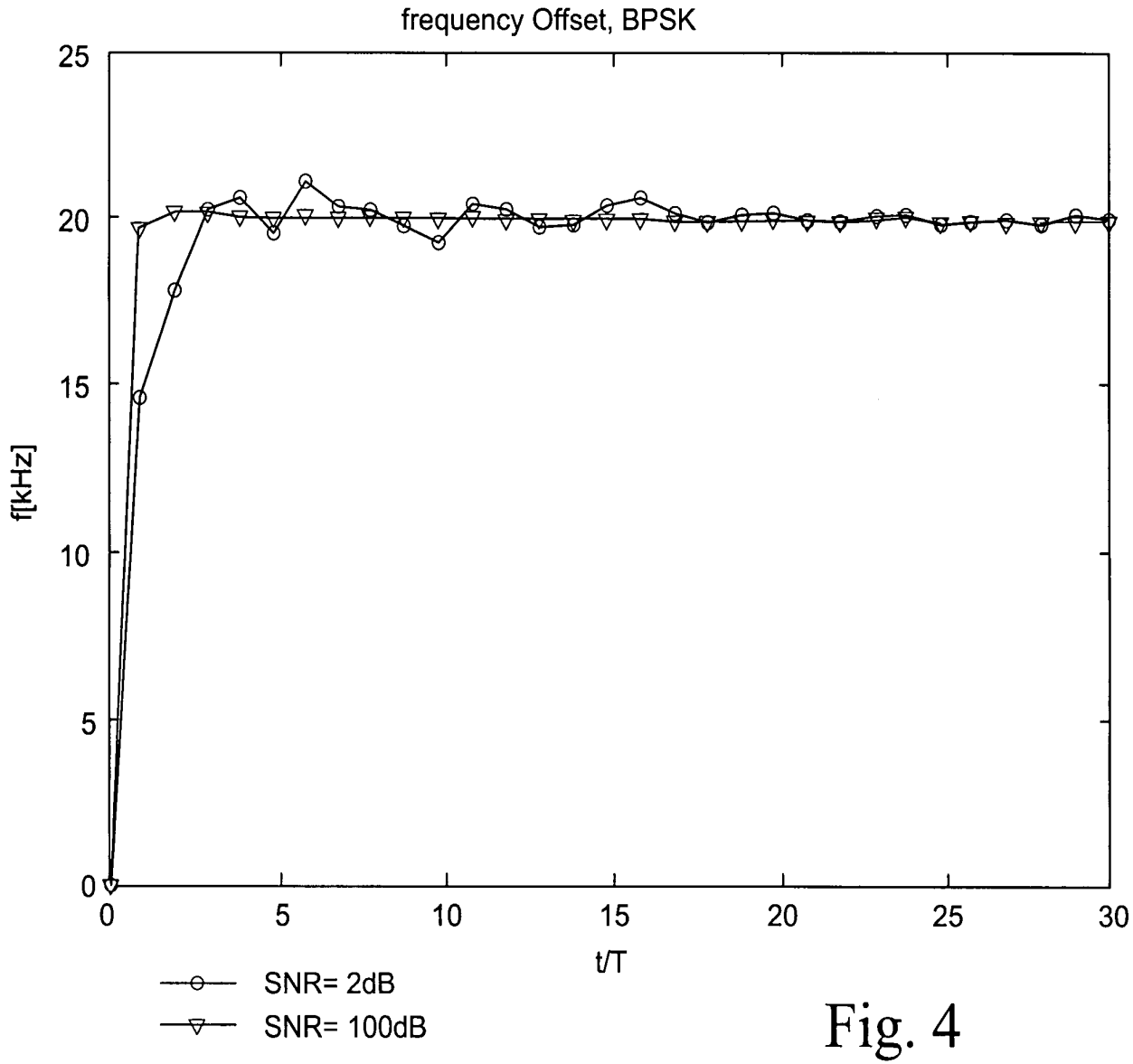
frequency Offset, BPSK
25
20
15
10
5
0
f[kHz]
0
5
10
15
20
25
30
t/T
SNR= 2dB
SNR= 100dB

Fig. 4